

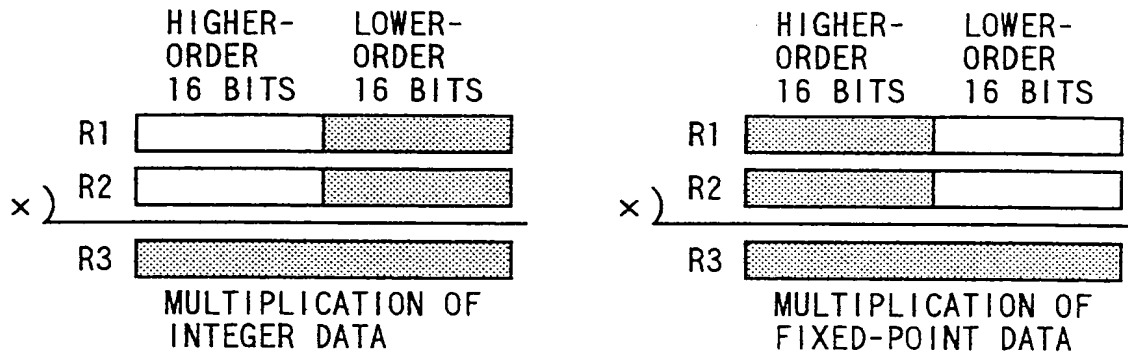
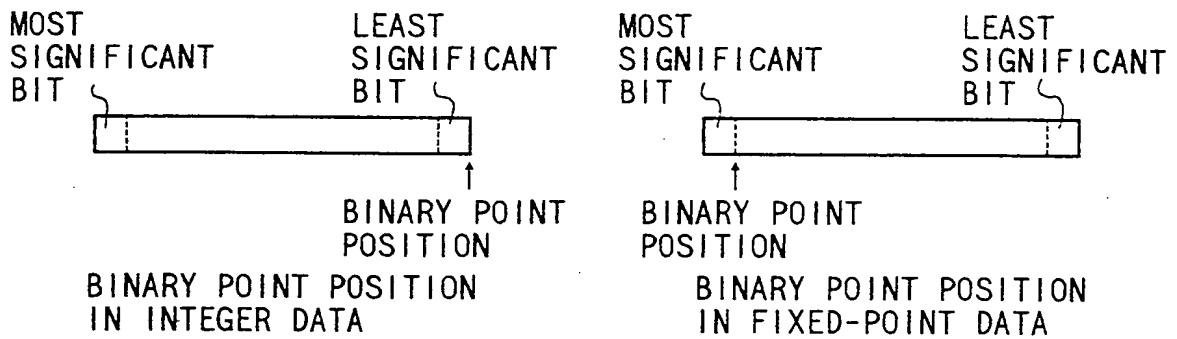
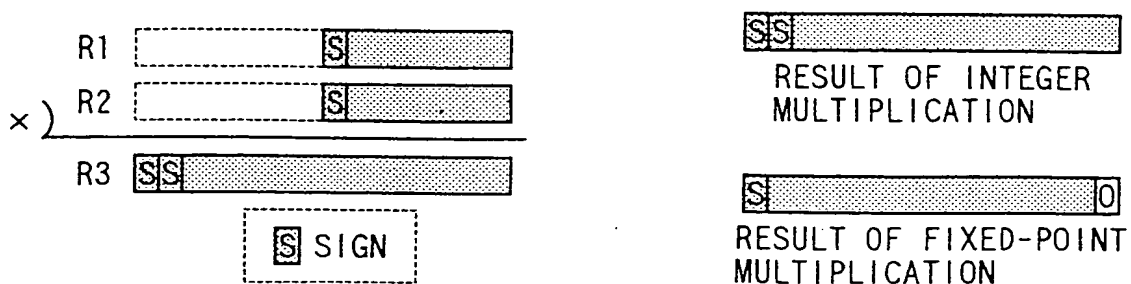
FIG. 1(a)
 $R1 \times R2 = R3$ (16 BITS \times 16 BITS = 32 BITS)
*FIG. 1(b)**FIG. 1(c)*

FIG. 2

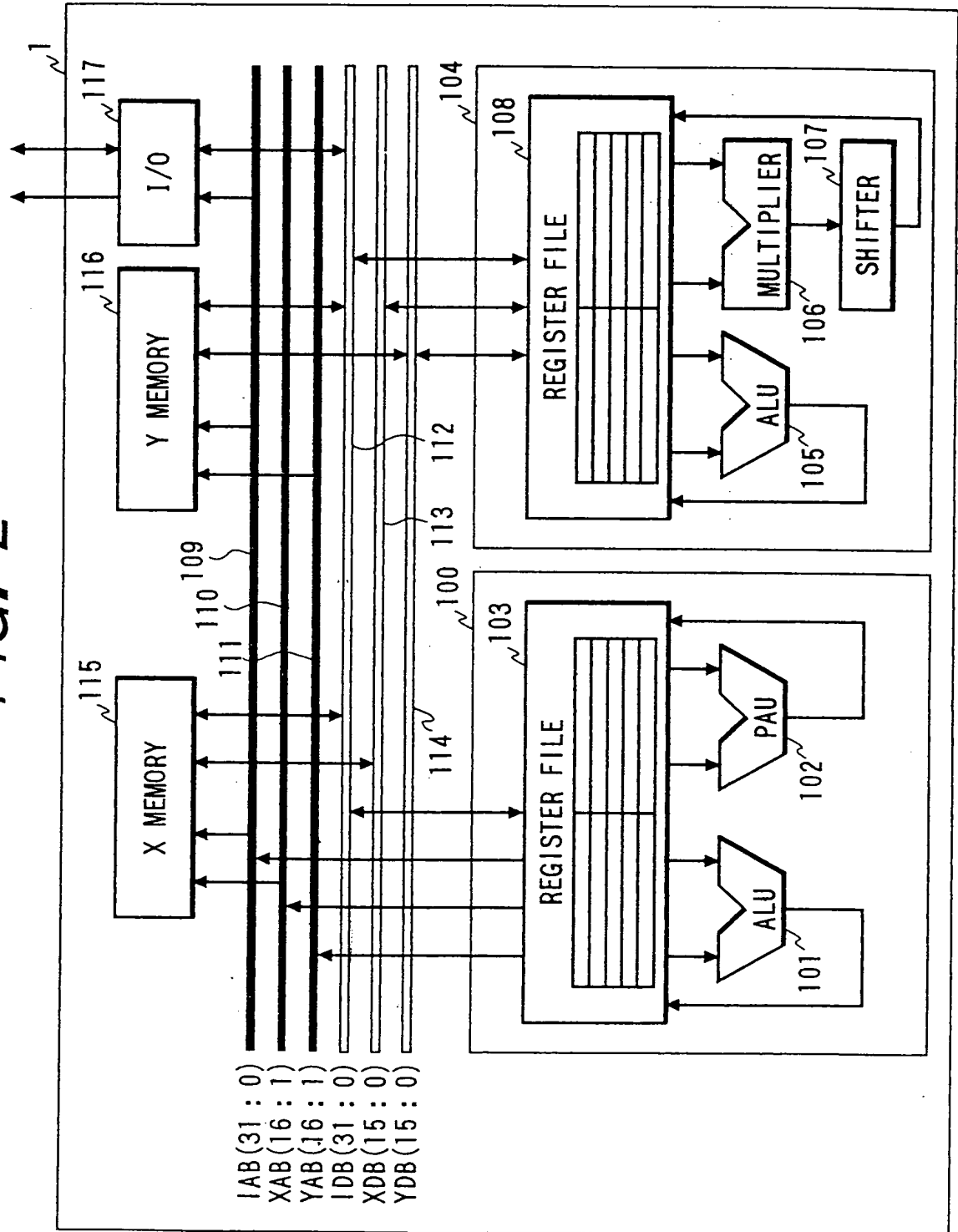


FIG. 4

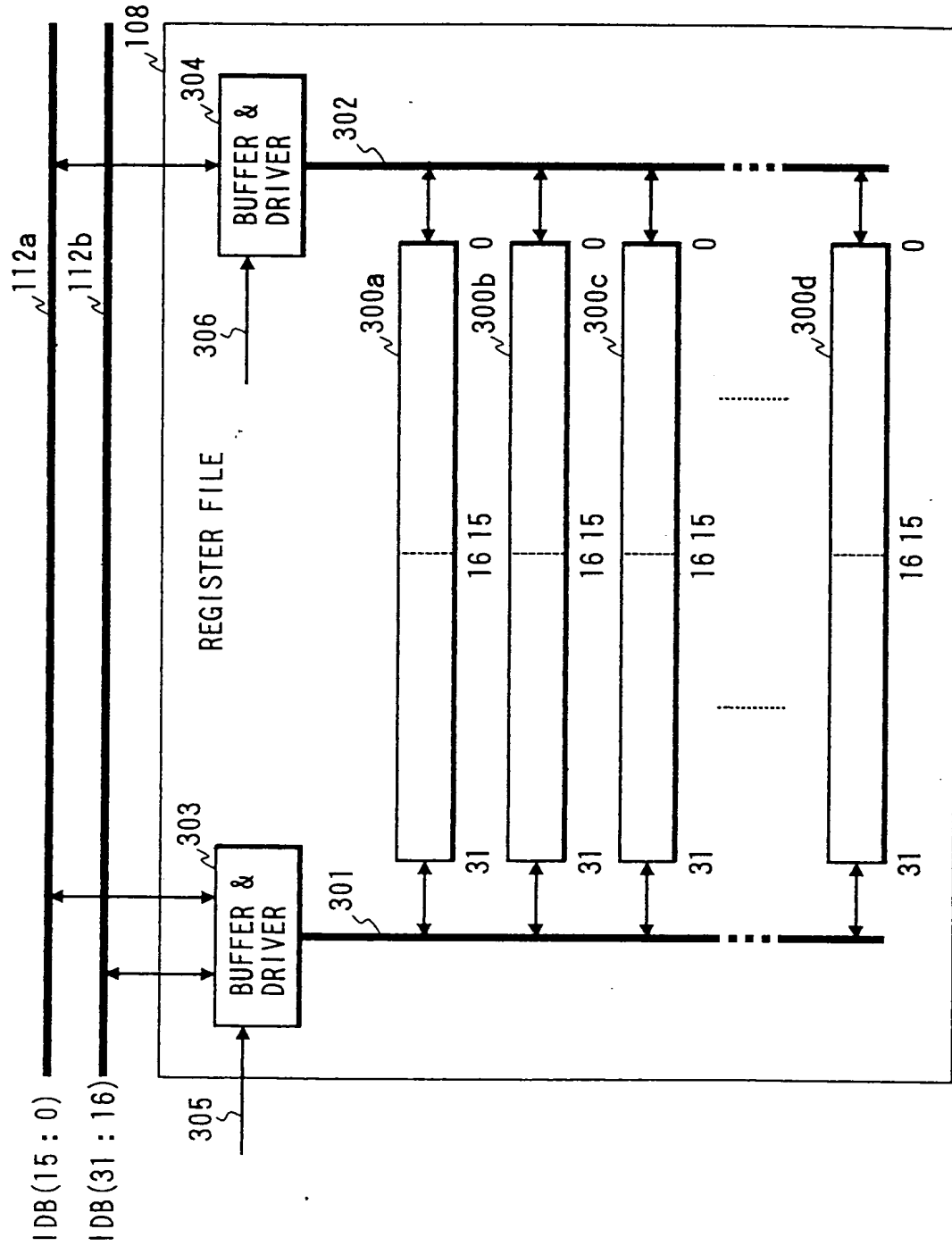


FIG. 5

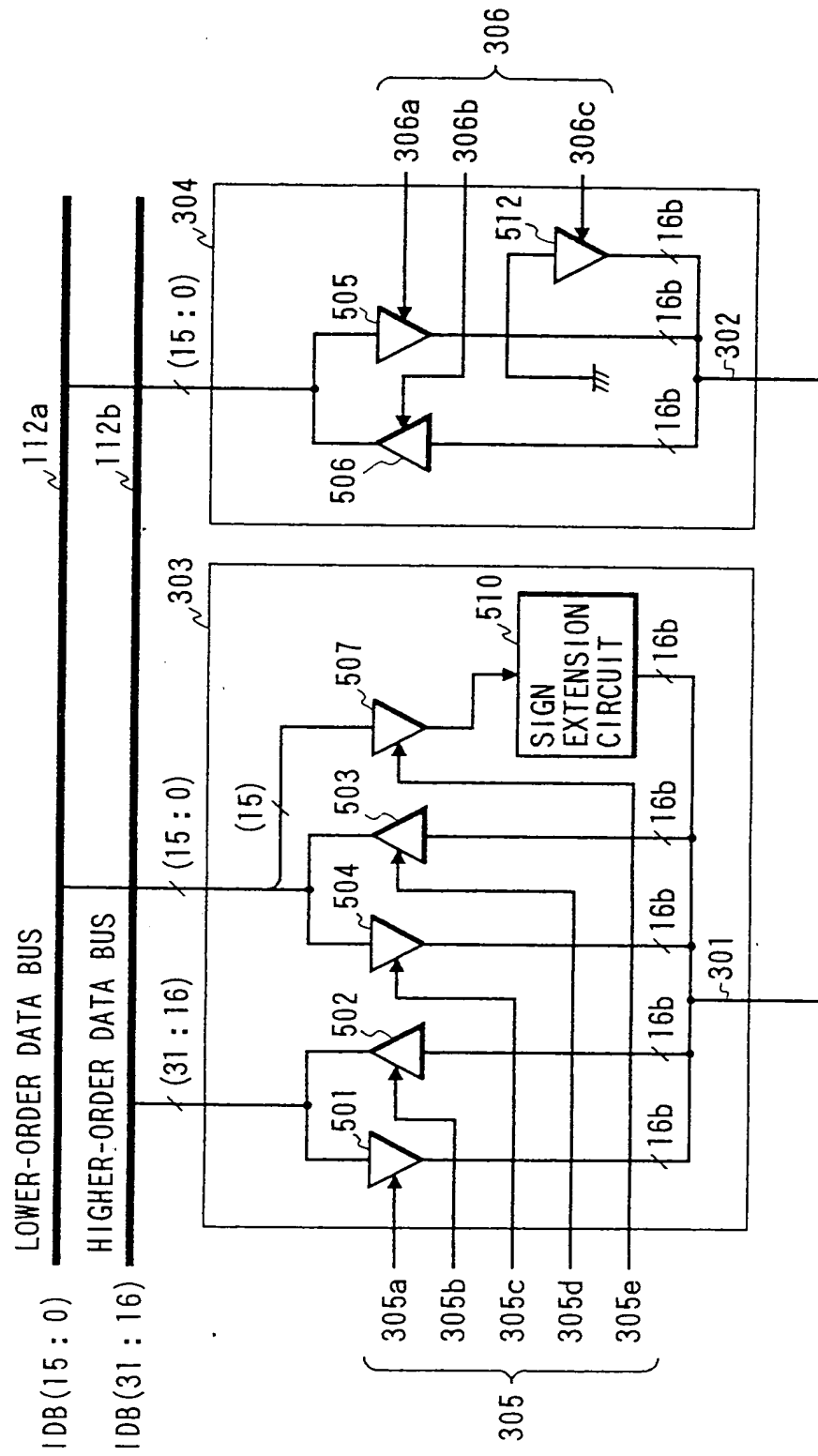


FIG. 6

	305a	305b	305c	305d	305e	306a	306b	306c
LOADING LONG WORD	1	0	0	0	0	1	0	0
STORING LONG WORD	0	1	0	0	0	0	1	0
INTEGER DATA	LOADING WORD	0	0	0	1	1	0	0
	STORING WORD	0	0	0	0	0	1	0
FIXED-POINT DATA	LOADING WORD	0	0	1	0	0	0	1
	STORING WORD	0	0	0	1	0	0	0

FIG. 7

